

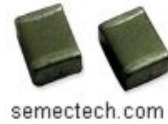
SCL Chip Multilayer Ceramic Capacitor

Semec

RoHS

Features

Tin/ Nickel termination to improve the solderability
 Multilayer block structure provides higher reliability
 Wide range of capacitance value available in standard sizes
 Widely used in electronic devices



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Specifications

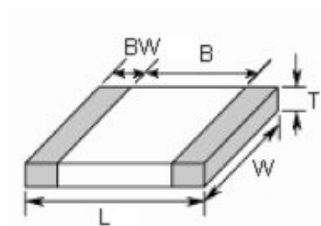
Rated Voltage	DC 16V, 25V, 50V
Capacitance Range	0.5pF to 10uF
Capacitance Tolerance	$\pm 0.25\text{pF}$, $\pm 0.5\text{pF}$, $\pm 5\%$, $\pm 10\%$, $\pm 20\%$, +80-20%
Temperature Characteristics	NPO: $-55^{\circ}\text{C} \sim +125^{\circ}\text{C}$ X7R: $-55^{\circ}\text{C} \sim +125^{\circ}\text{C}$ Y5V: $-25^{\circ}\text{C} \sim +85^{\circ}\text{C}$

Dimensions

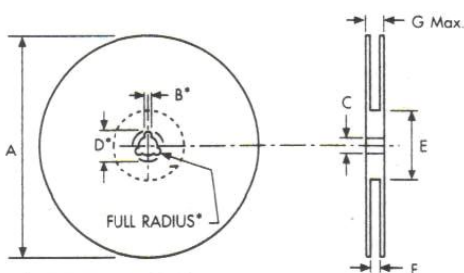
unit:mm

size	L	W	T (Max)	bw
0402	1.00 ± 0.05	0.50 ± 0.05	0.55	0.25 ± 0.10
0603	1.60 ± 0.10	0.80 ± 0.10	0.95	0.35 ± 0.15
0805	2.00 ± 0.20	1.25 ± 0.20	1.4	0.45 ± 0.25
1206	3.20 ± 0.30	1.60 ± 0.20	1.8	0.50 ± 0.20
1210	3.20 ± 0.30	2.50 ± 0.20	2.7	0.60 ± 0.30

Structure



Package



- A: $178 \pm 2.0\text{mm}$
- B: 3.0mm
- C: $13 \pm 0.5\text{mm}$
- D: 32mm
- E: $78 \pm 1\text{mm}$
- F: $10 \pm 1.5\text{mm}$
- G: 14.9mm MAX

* DRIVE SPOKES OPTIONAL
 IF USED ASTERISKED
 DIMENSIONS APPLY.

* The spec is subject to change without notice. Please contact us if any further queries.